



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani, et al
Serial No. : 08/690,747
Filed : August 1, 1996
Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Art Unit : 1765
Examiner : Robert Kunemund

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT SUBMITTED WITH REQUEST FOR CONTINUED EXAMINATION
UNDER 37 CFR 1.114

Please amend the above-identified application as follows:

Amendments to the Claims begin at page 2 of this paper.

Remarks/Arguments begin on page 16 of this paper.